Ref	Hits	Search Query	DBs	Default	Plurals	Time Sta
# L1	2	"6614659".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	Operator OR	ON	2005/07
L2	2	"6542365".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07
L3	2	"6462413".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07
L4	2	"6670216".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07
L5	2	"20020163070".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB		ON	2005/07
L6	2	"4902854".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB		ON	2005/07

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Ref	Hits	Search Query ·	DBs	Default Operator	Plurals	Time S
# L1	20450	multichip (multi adj chip)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/0
L2	9482	1 with (component circuit assembly)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/0
L3	415255	(plurality multiple) with (carrier substrate board pcb ((Printed wiring circuit) adj3 board))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB		ON	2005/0
L4	1274	2 same 3	US-PGPUB USPAT; USOCR; EPO; JPO; DERWENT IBM_TDB	; r;	ON	
L5	603	ceramic and 4	US-PGPUE USPAT; USOCR; EPO; JPO DERWEN IBM_TDB	B; OR O; T;	ON	2005/07
L6	13	((transistor diode bipolar (bi adj policy) (field adj effect)) and 5	1	OR O; NT;	ON	2005/07
L7	11	(leadframe lead) and 6	US-PGPU USPAT; USOCR; EPO; JP DERWE IBM_TD	JB; OR O; NT;	ON	2005/0